Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A multilayer capacitor wherein a plural number of internal conductors are respectively disposed between dielectric sheets in dielectric body formed by laminating a plural number of dielectric ~~sheets wherein;~~sheets,

the internal conductors ~~comprising;~~comprising:

at least a pair of first internal conductors respectively led out toward two facing side surfaces of dielectric sheets, and

at least a pair of second internal conductors respectively led out toward two facing side surfaces of the dielectric sheets different from the two facing side surfaces where said first internal conductors are led out, wherein

at least one of the pair of the second internal ~~conductor~~ conductors is arranged between a pair of the first internal conductors intervening said ~~dielectric sheets, and~~ dielectric sheets,

at least one of the pair of the first internal ~~conductor~~ conductors is arranged between a pair of the second internal conductors intervening said ~~dielectric sheets.~~dielectric sheets,

at least a pair of first terminal electrodes arranged at two facing side surfaces of the dielectric body and respectively connected to a pair of the first internal conductors,

a pair of second terminal electrodes arranged at two facing side surfaces of the dielectric body, different from the two facing side surfaces where said first internal conductors are arranged, and respectively connected to a pair of the second internal conductors,

at least one of the first internal conductor and the second internal conductor comprises a plural number of segmented conductors alternately led out toward two facing side surfaces of the dielectric body segmented in the way that the conductors extend in parallel form,

mutually adjoining segmented conductors arranged in the same plane are respectively connected to the terminal electrodes respectively arranged at two facing side surfaces, and

a lead part connected to the terminal electrode is formed in the segmented conductor, at least three segmented conductors are arranged in a plane, and two of the three segmented conductors arranged in every other segmented conductor are connected through said lead part.

2-5. (Canceled)

6. (Currently Amended) The multilayer capacitor as set forth ~~claim 2~~ in claim 1, wherein lead parts respectively connected to the first terminal electrode and the second terminal electrode are formed in the first internal conductor and the second internal conductor.

7. (Canceled)

8. (Currently Amended) The multilayer capacitor as set forth in ~~claim 7~~ claim 1, ~~Wherein~~ wherein width of the lead parts arranged in a plane facing each other are nearly the same.

9. (Currently Amended) The multilayer capacitor as set forth in ~~claim 3~~ claim 1, wherein a planar shape of the segmented conductor is rectangle, triangle, or trapezoid.

10. (Currently Amended) A multilayer capacitor wherein a plural number of internal conductors are respectively disposed between dielectric sheets in dielectric body

formed by laminating a plural number of dielectric sheets, ~~wherein~~ the internal conductors ~~comprising;~~comprising:

at least a pair of first internal conductors respectively led out toward two facing side surfaces of dielectric sheets, and

at least a pair of second internal conductors respectively led out toward two facing side surfaces of the dielectric sheets different from the two facing side surfaces where said first internal conductors are led out, wherein

at least one of the pair of the second internal conductors~~conductor~~ is arranged between a pair of the first internal conductors intervening said dielectric sheets,

at least one of the pair of the first internal conductors~~conductor~~ is arranged between a pair of the second internal conductors intervening said dielectric sheets,

the at least one of the pair of the first internal conductors~~conductor~~ comprises a plural number of segmented conductors wherein the conductors are segmented to extend mutually in a row and are alternately led out toward two facing side surfaces of dielectric body, ~~and~~

the at least one of the pair of the first internal conductors mutually adjoining in the laminated direction disposing the second internal conductor in between are arranged to superpose upon each other when observed from planner view, the segmented conductors that superpose upon each other when observed from planner view are alternately led out toward the opposite ~~directions.~~directions,

mutually adjoining segmented conductors arranged in the same plane are respectively connected to the terminal electrodes respectively arranged at two facing side surfaces, and

a lead part connected to the terminal electrode is formed in the segmented conductor, at least three segmented conductors are arranged in a plane, and two of the three

segmented conductors arranged in every other segmented conductor are connected through said lead part.

11. (Currently Amended) The multilayer capacitor as set forth in ~~claim 10~~ claim 10, wherein the second internal conductors are not segmented.

12. (Currently Amended) The multilayer capacitor as set forth in ~~claim 10~~ claim 10, having;

a plural pairs of the first terminal electrodes respectively connected to a plural number of segmented conductors and are respectively led out toward two facing side surfaces of the dielectric body, and

a pair of the second terminal electrodes respectively connected to a pair of the second internal conductor and respectively led out toward two facing side surfaces of dielectric body different from two facing side surfaces where plural pairs of the first terminal electrodes are led out.

13. (Currently Amended) The multilayer capacitor as set forth in ~~claim 1~~ claim 1, wherein the dielectric body is in a shape of rectangular parallelepiped.

14. (Previously Presented) The multilayer capacitor as set forth in claim 1, wherein plural pairs of the first and the second internal conductors are arranged in the laminated direction respectively in the dielectric body.